

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(system adj on adj chip) and ((disk or disc) adj controller) and (waveform adj generator) and simulat\$6	US-PGPU B	OR	ON	2006/01/16 09:44
L2	0	((system adj on adj chip) or soc or system-on-chip) and ((disk or disc) adj controller) and (waveform adj generator) and simulat\$6	US-PGPU B	OR	ON	2006/01/16 09:46
L5	16	((system adj on adj chip) or soc or system-on-chip) and ((disk or disc) adj controller) and simulat\$6	US-PGPU B	OR	ON	2006/01/16 09:56
L6	199	((arbitrary adj waveform adj generator) or (waveform adj generator)) and (adder or (add\$3 adj circuit))	US-PGPU B	OR	ON	2006/01/16 09:54
L8	10	((system adj on adj chip) or soc or system-on-chip) and 6	US-PGPU B	OR	ON	2006/01/16 09:59
L9	0	(first adj soc) and (second adj soc) and (read adj channel)	US-PGPU B	OR	ON	2006/01/16 10:01
L10	0	5 and (read adj channel)	US-PGPU B	OR	ON	2006/01/16 10:02
L11	28	6 and (timing adj signal)	US-PGPU B	OR	ON	2006/01/16 10:03